



PATENT APPLICATION *RECEIVED
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

the Application of

Terunao HANAOKA

Group Art Unit: 2822

Application No.: 10/679,467

Examiner: T. TRAN

Filed: October 7, 2003

Docket No.: 117462

For: SEMICONDUCTOR WAFER,
SEMICONDUCTOR DEVICE AND METHOD
OF MANUFACTURING THE SAME,
CIRCUIT BOARD, AND ELECTRONIC
EQUIPMENT

MAIL STOP RCE

**LARGE ENTITY REQUEST FOR
CONTINUED EXAMINATION UNDER 37 C.F.R. §1.114**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the provisions of 37 C.F.R. §1.114, Applicant(s) hereby request(s) continued examination.

Applicant further requests entry and consideration of the attached submission.

The above-identified application was filed on or after June 8, 1995. Thus, entry is proper under 37 C.F.R. §1.114(d).

Attached hereto is our check no. 178320 in the amount of \$790.00 as payment of the fees set forth in 37 C.F.R. §1.17(e). The Commissioner is hereby authorized to charge any additional fees or credit any overpayment associated with this communication to Deposit Account No. 15-0461. Two duplicate copies of this page are enclosed.

Respectfully submitted,


James A. Oliff
Registration No. 27,075

Gang Luo
Registration No. 50,559

JAO:GXL/sqb

Date: March 29, 2006

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**DEPOSIT ACCOUNT USE
AUTHORIZATION**
Please grant any extension
necessary for entry;
Charge any fee due to our
Deposit Account No. 15-0461

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